

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:  
H. Bernhard Pogge *et al.*

Examiner: Not yet assigned

Application No.: Not yet assigned

Group Art Unit: Not yet assigned

Filed: Herewith

For: INTEGRATED ELECTRONIC  
CHIP AND INTERCONNECT  
DEVICE AND PROCESS FOR  
MAKING THE SAME

Date: June 8, 2004

This application is a division of  
Application No. 10/605,204  
filed September 15, 2003

**PRELIMINARY AMENDMENT**

The Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313

Sir:

Prior to examination on the merits, please amend the above-identified application as follows:

FIS9-2002-0007-US2